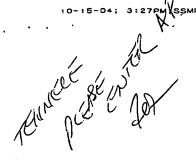
8/ 17



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Response Under 37 C.F.R. §1.116
Expedited Procedure Examining
Examining Group 2123

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Daniel R. Knebel, et al.

Examiner: Fred O. Ferris, III

Serial No.: 09/406,663

Art Unit: 2123

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For: SYSTEM AND METHOD FOR

VLSI VISUALIZATION

Dated: October 15, 2004

Confirmation No.: 1830

Commissioner for Patents

P.O. Box 1450

Arlington, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. §1.116

Sir:

In response to the Office Action dated August 16, 2004, please amend the subject patent application as follows.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: October 15, 2004

John & Sensing John S. Sensny